

FIGURE 1

200

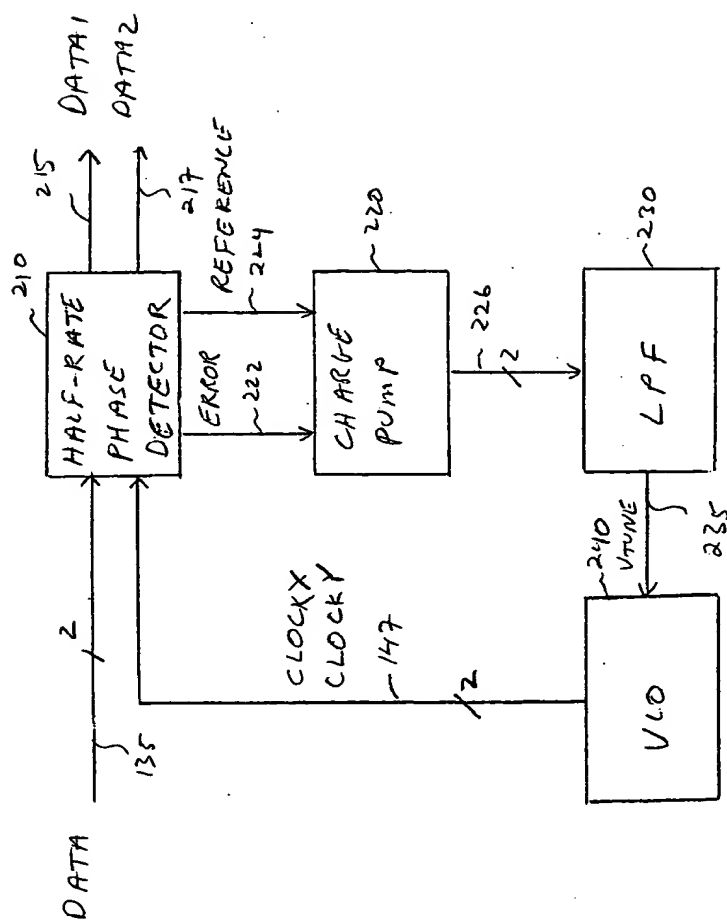
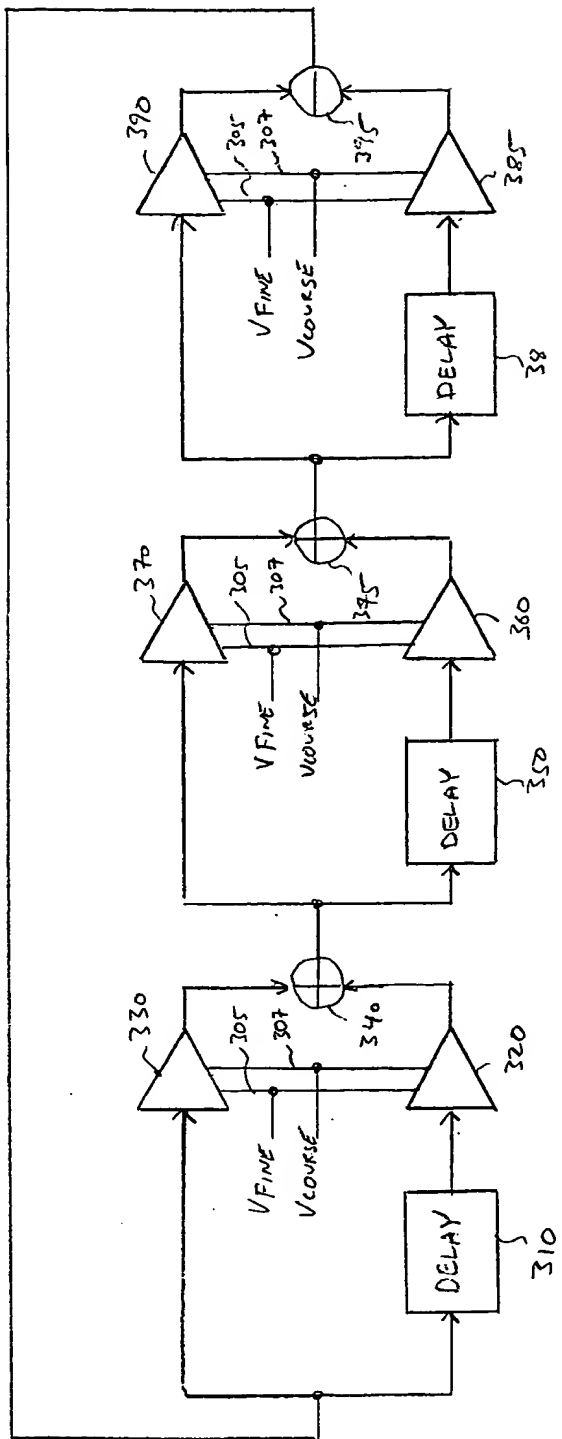


FIGURE 2



300

FIGURE 3

400

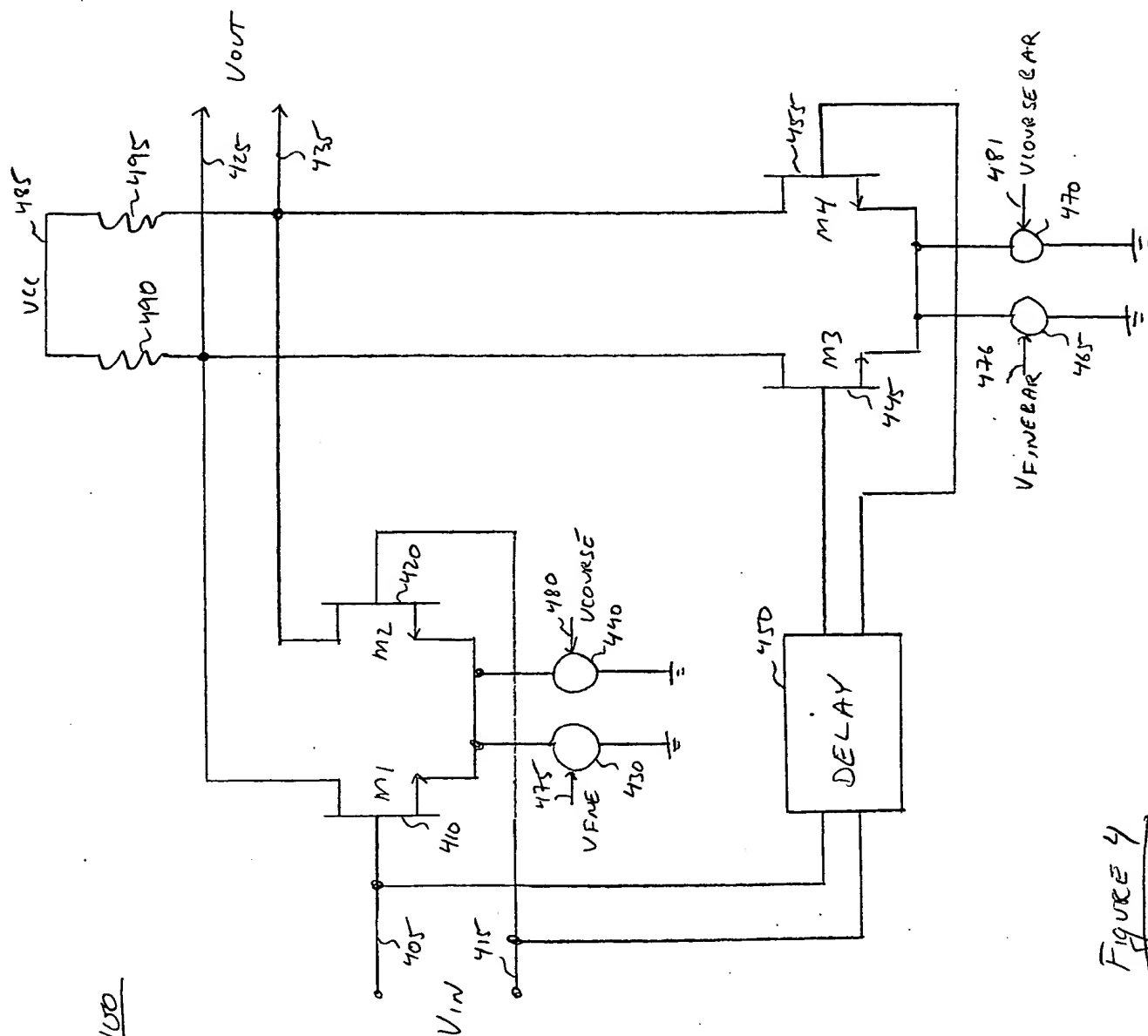


Figure 4

500

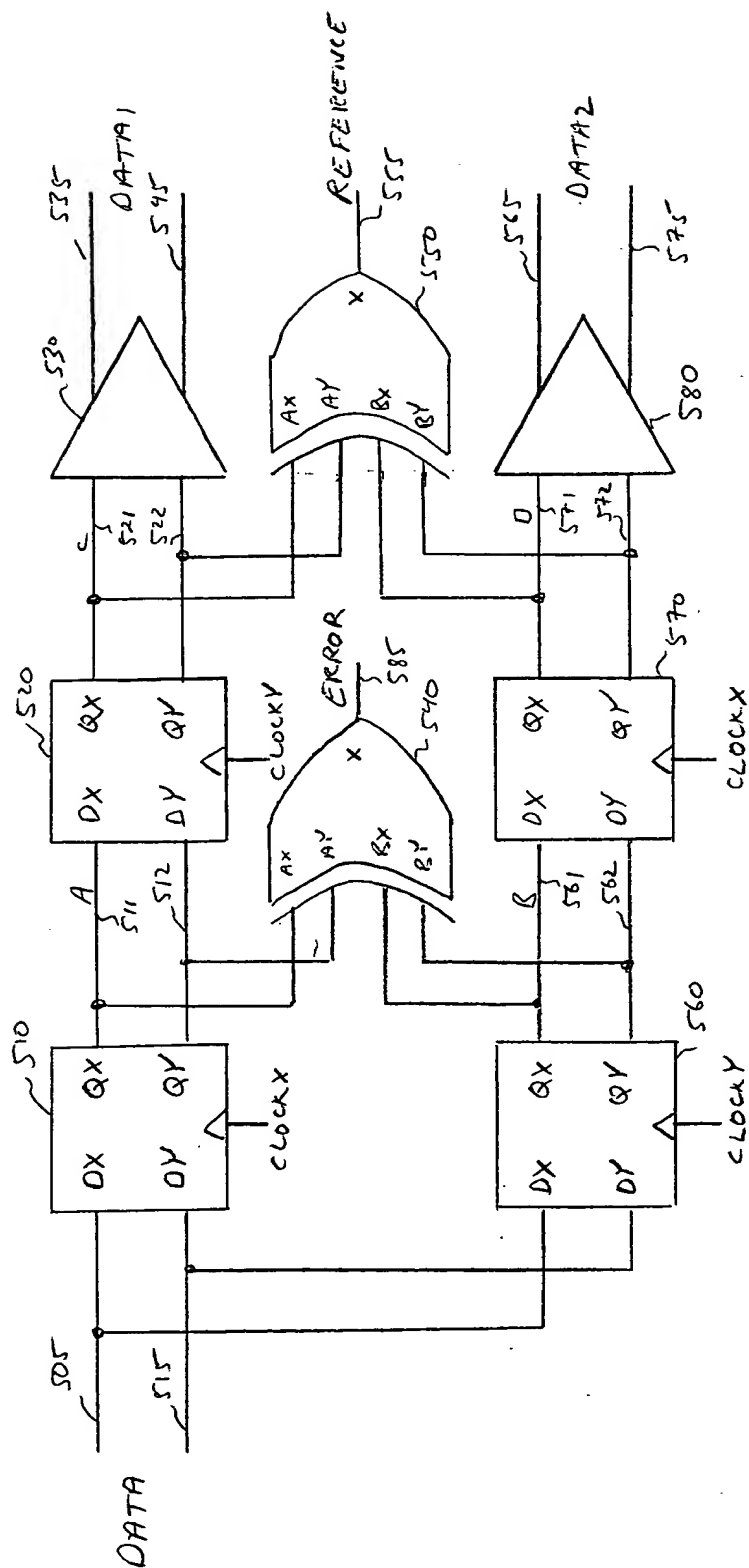


FIGURE 5

600

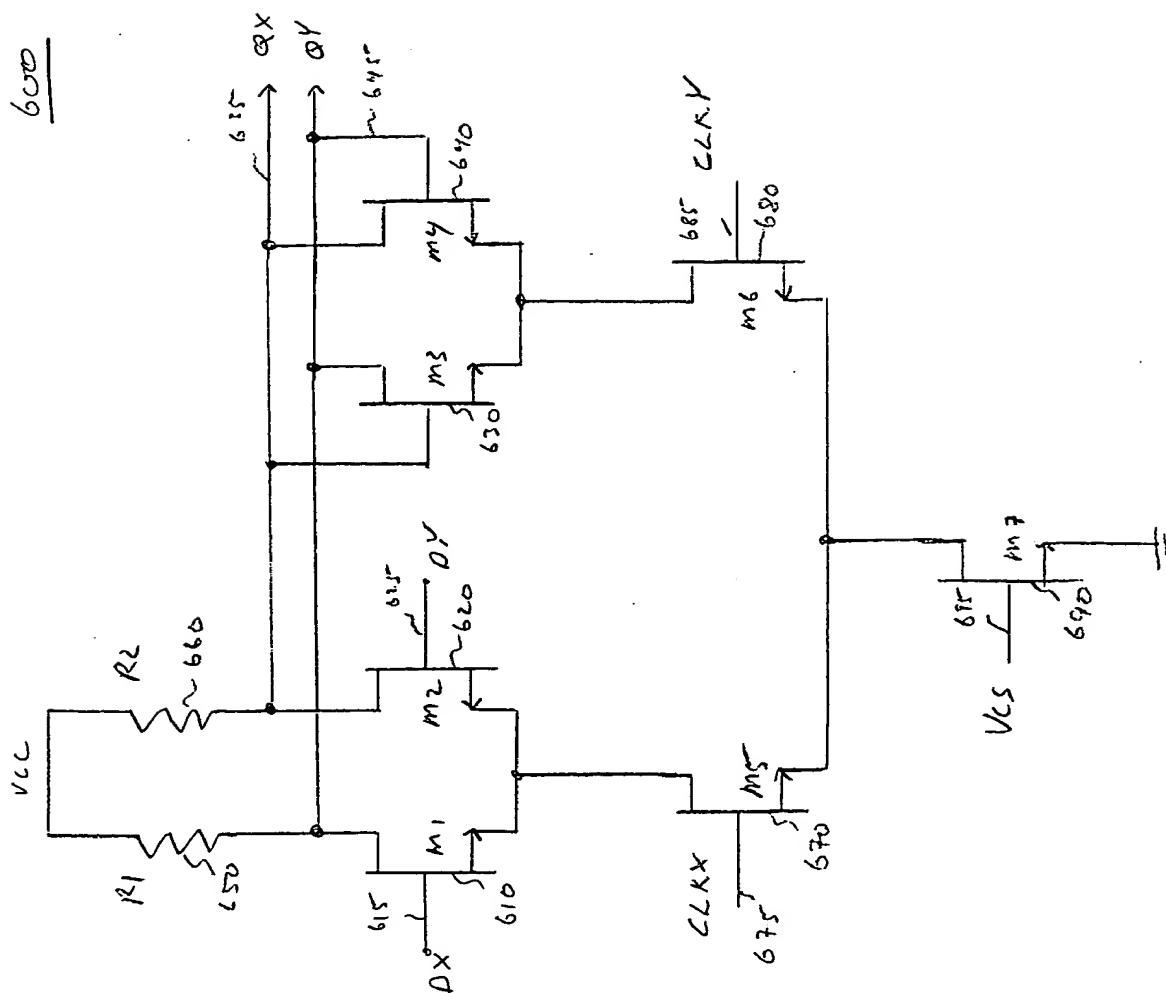
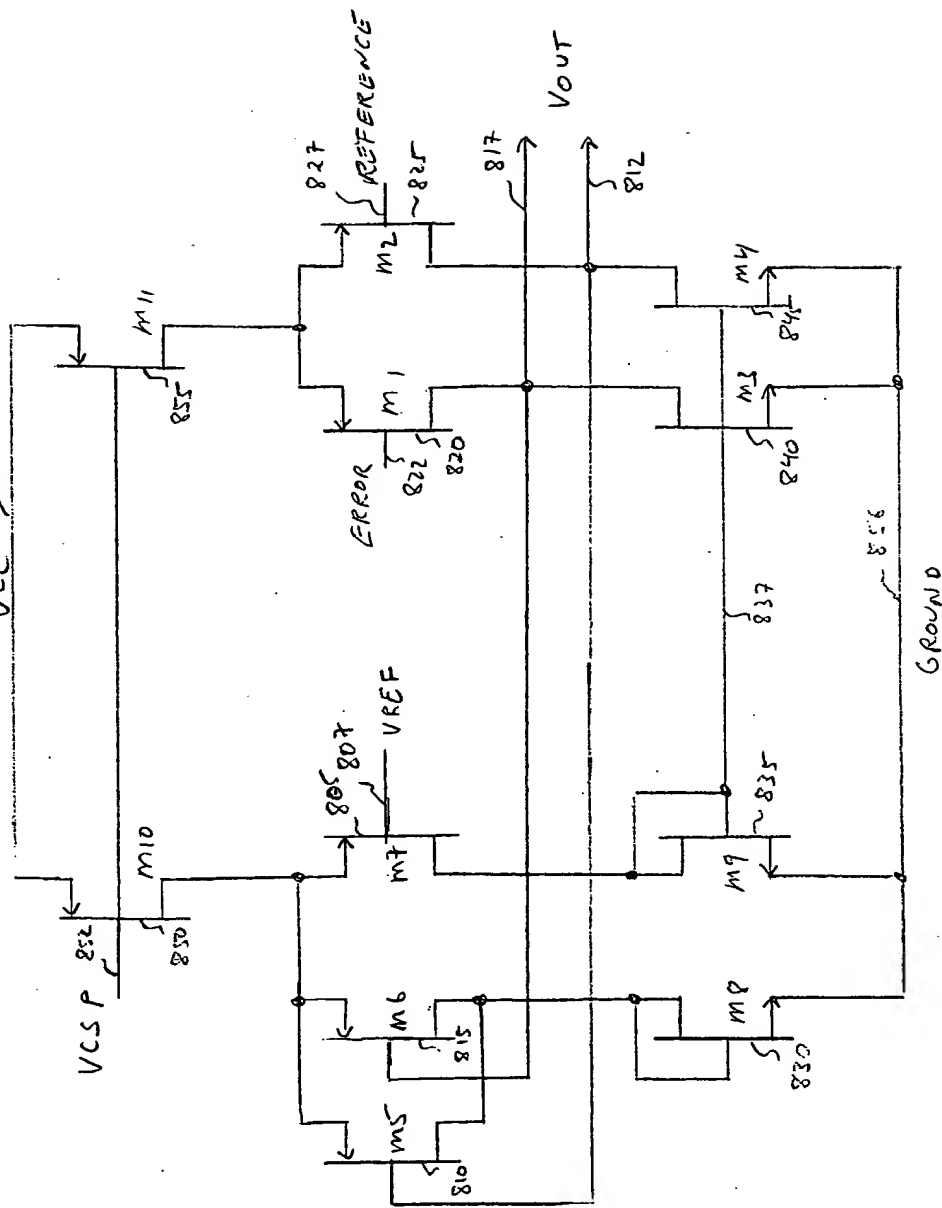


Figure 6





800

Figure 8



# FIGURE 9

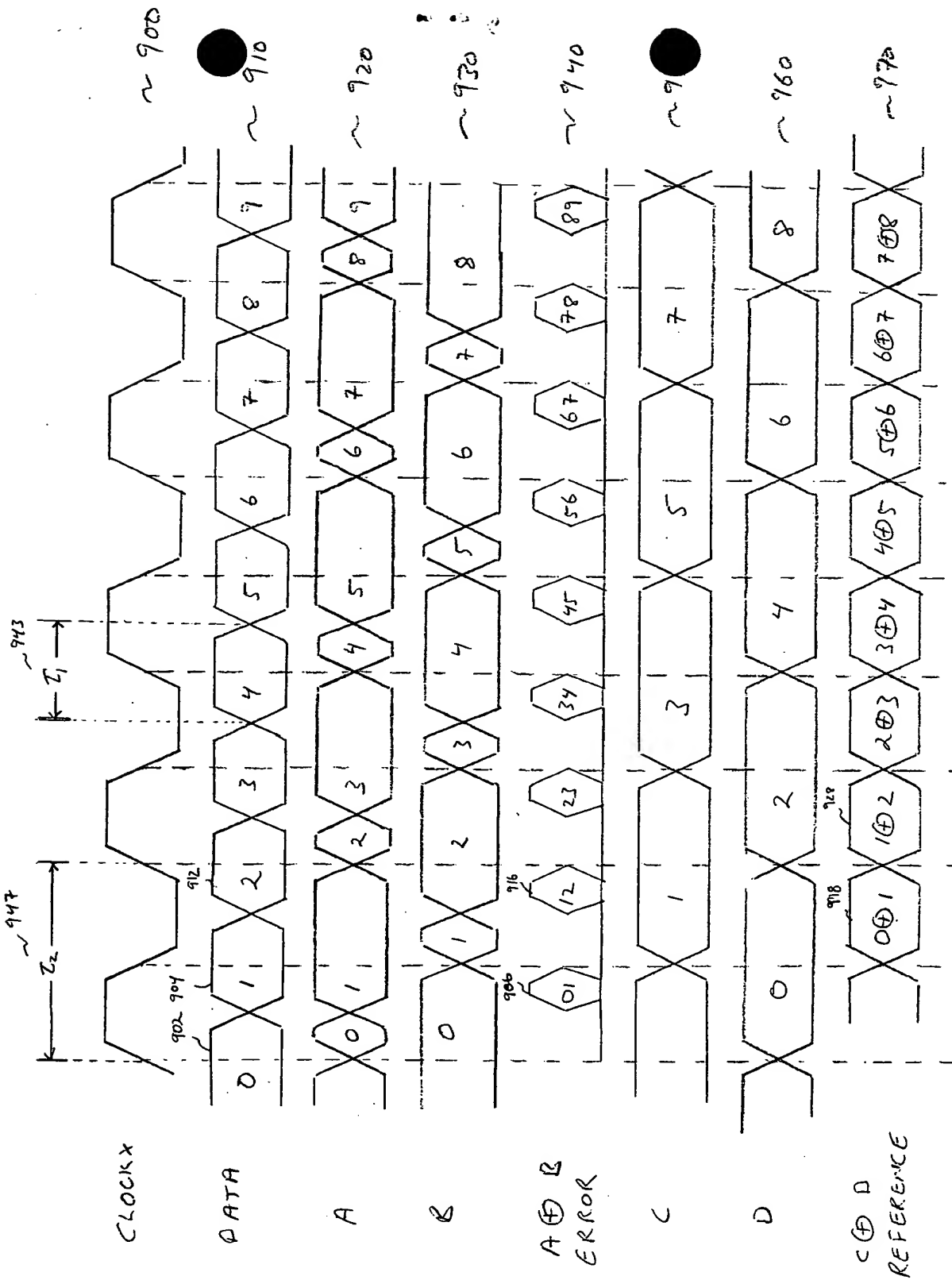


Figure 9

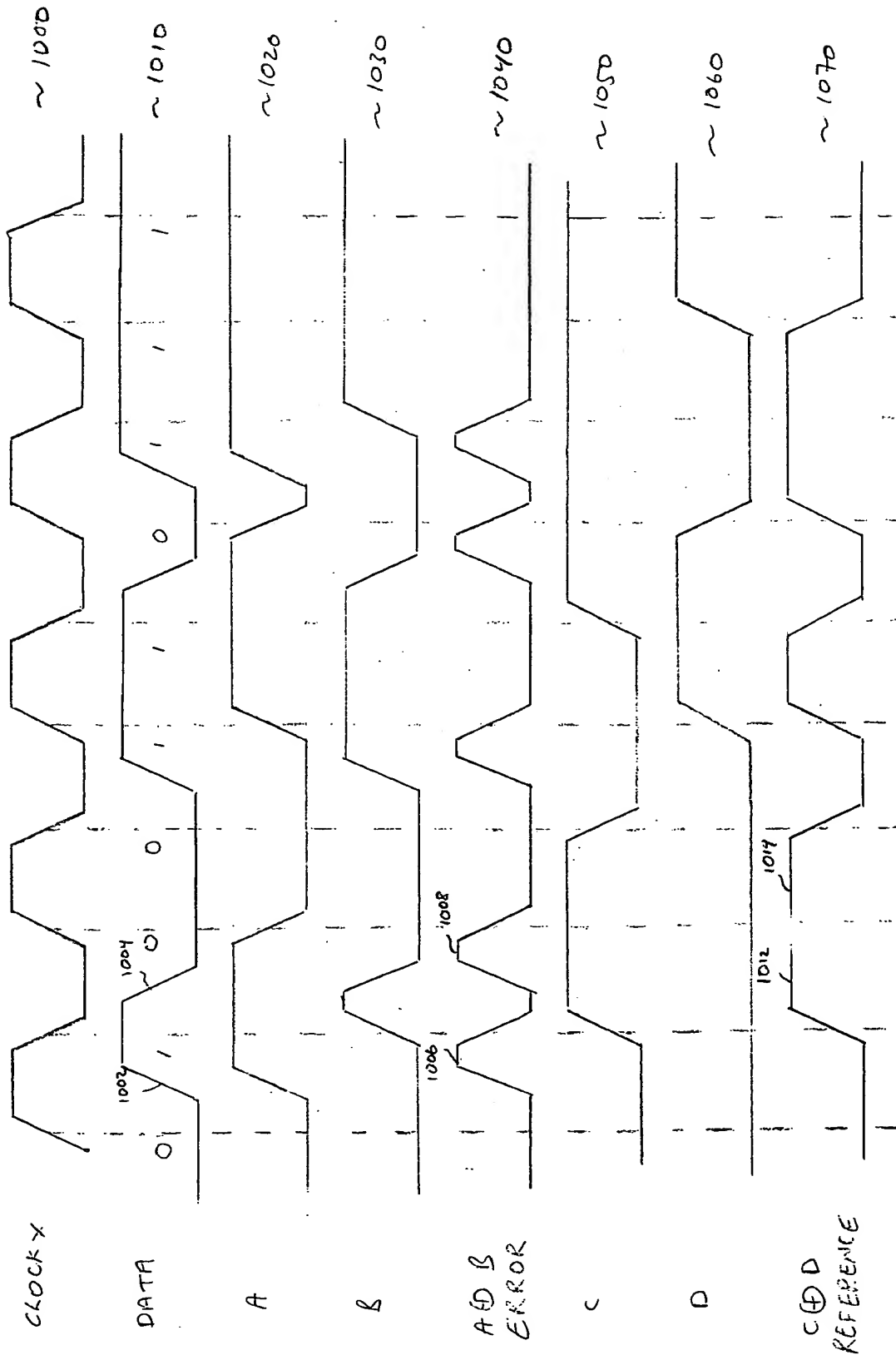


Figure 10

Timing diagram showing waveforms for CLOCK, DATA, A, B,  $A \oplus B$  ERROR, C, D, and  $C \oplus D$  REFERENCE. The diagram includes address labels (1100 to 1170) and data labels (1102, 1104, 1106, 1108, 1112, 1114) indicating specific data points or errors.

[illegible]

1200

1200

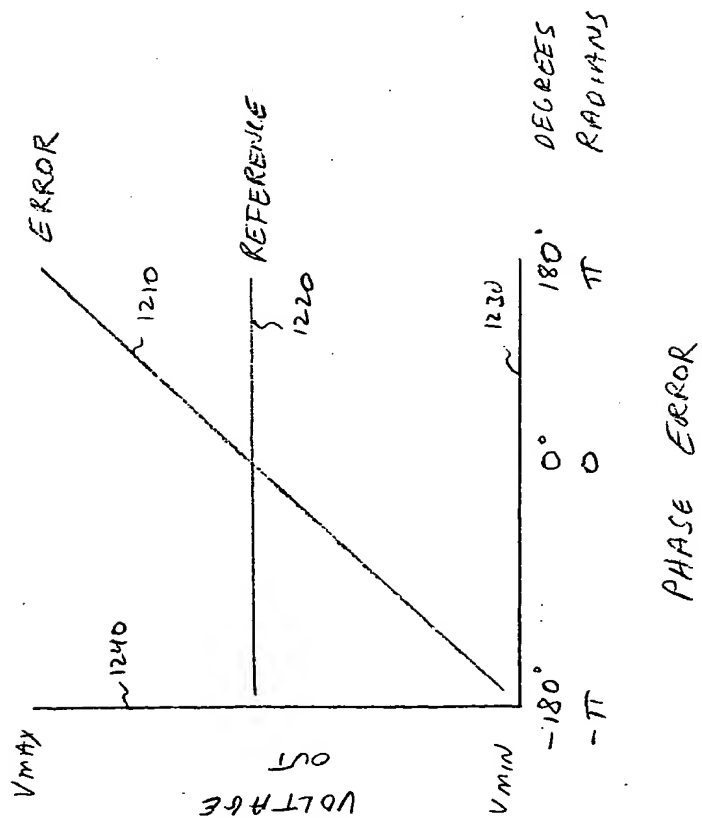


FIGURE 12

PROVIDE AN INPUT DATA SIGNAL, A CLOCK SIGNAL, AND A COMPLEMENTARY CLOCK SIGNAL.

1310

APPLY THE INPUT DATA TO A FIRST LATCH CLOCKED BY THE CLOCK SIGNAL.

1320

APPLY THE INPUT DATA TO A SECOND LATCH CLOCKED BY THE COMPLEMENTARY CLOCK SIGNAL.

1330

APPLY THE OUTPUT OF THE FIRST LATCH TO A FIRST XOR GATE AND A THIRD LATCH.

1340

APPLY THE OUTPUT OF THE SECOND LATCH TO THE FIRST XOR GATE AND A FOURTH LATCH.

1350

APPLY THE OUTPUT OF THE THIRD LATCH AND THE FOURTH LATCH TO A SECOND XOR GATE.

1360

USE THE OUTPUT OF THE FIRST XOR GATE AS AN ERROR SIGNAL, THE OUTPUT OF THE SECOND XOR GATE AS A REFERENCE SIGNAL, THE OUTPUT OF THE THIRD LATCH AS A FIRST DATA OUTPUT, AND THE OUTPUT OF THE FOURTH LATCH AS A SECOND DATA OUTPUT.

1370

SUBTRACT THE ERROR SIGNAL FROM  $1/2$  THE REFERENCE SIGNAL, AND FILTER.

1380

USE FILTER OUTPUT TO ADJUST CLOCK AND COMPLEMENTARY CLOCK SIGNALS.

1390

FIGURE 13